

# *Microcontroller Solutions*

```
DELAY:MOV R3,#150          1 machine cycle
HERE:NOP                    1 mc
NOP                          1
NOP                          1
DJNZ R3,HERE                2 mc
    RET                    2
```

The time delay of the HERE loop is  $[150(2+1+1+1)] \times 1.085\mu\text{sec} = 0.813\text{msec}$

Now for the instruction outside the loop (mov and ret),

$$(2+1) \times 1.085\mu\text{sec} = 3.25\mu\text{sec}$$

$$\text{Now } 0.813\text{ms} + 3.25\mu\text{sec} = 0.8162\text{msec}$$

33.  $F = 16\text{MHz}$   
 $F = 16\text{MHz}/12 = 1.33333\text{MHz}$   
 $T = 1/F = 1/1.33333\text{MHz}$   
 $T = 0.75\mu\text{sec}$

```
DELAY:MOV R3,#200          1
HERE:NOP                    1
    NOP                    1
    NOP                    1
    DJNZ R3,HERE            2
    RET                    2
```

The time delay of the HERE loop is  $[200(2+1+1+1)] \times 0.75\mu\text{sec} = 0.75\text{msec}$

Now for the instruction outside the loop (mov and ret),

$$(2+1) \times 0.75\mu\text{sec} = 2.25\mu\text{sec}$$

$$\text{Now } 0.75\text{ms} + 2.25\mu\text{sec} = 0.75225\text{msec}$$

34.  $F = 11.0592\text{MHz}$   
 $F = 11.0592\text{MHz}/12 = 921.6\text{KHz}$   
 $T = 1/F = 1/921.6\text{KHz}$   
 $T = 1.085\mu\text{sec}$

# Microcontroller Solution Manual

**Steven Barrett, Daniel Pack**



## **Microcontroller Solution Manual:**

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Programmable Microcontrollers with Applications Cem Unsalan, H. Deniz Gurhan, 2013-08-29

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**Microcontroller Databook** National Semiconductor Corporation,1988 **XIII SBMicro, International Conference on Microelectronics and Packaging, ICMP'98: Technical papers** Ivan Jorge Chueiri,Carlos Alberto dos Reis Filho,1998

*Mechatronics with Experiments* Sabri Cetinkunt,2014-11-26 Comprehensively covers the fundamental scientific principles and technologies that are used in the design of modern computer controlled machines and processes Covers embedded microcontroller based design of machines Includes MATLAB Simulink based embedded control software

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**Instrumentation & Control Systems** ,1998 International Workshop on Electronic Design, Test and Applications Michel Renovell,2002 A collection of the 78 oral presentations and 24 poster papers from the January 2002 international workshop which brought together specialists from a broad area of electronic design manufacturing test and advanced system applications in the hope that the conference would integrate design test and application as cross dependent disciplines The contributions are organized into sessions focusing on analog test communications digital signal processing and architectures low to high level fault simulation and identification high level design memory power issues in design and test sensor and analog design electrical engineering education electromagnetics and control fault tolerant digital systems image processing robotics submicron technology test generation and compaction and test techniques and methodologies Annotation copyrighted by Book News Inc Portland OR

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